

MODEL QUESTION PAPER

DSE5/DSET5 - L

III Semester DIPLOMA Examination, August 2011 ELECTRONIC DEVICE LAB

Time: 01 Hours

Max.Marks: 25

- Q.1 Explain Negative feedback amplifier.
- Q.2 Explain with neat diagram LC filter
- Q.3 What can you say about VI characteristics of Regulator?
- Q.4 Write about the JFET characteristics.
- Q.5 Write about the input/output characteristics of CE Transistors.
