MODEL QUESTION PAPER

<div align="right">**DSCO4 / DSET4**</div>

III Semester DIPLOMA Examination, August 2011
MICROPROCESSOR

Time: 3 Hours   Max. Marks: 75

GROUP A : Answer any three questions.

Q.1   What is program counter? Explain in details.
Q.2   Explain FLAG register in details.
Q.3   Draw and explain block diagram of 8085 µp .
Q.4   What are the various computer generations? Discuss main features of each generation.
Q.5   What is control and data bus, Explain function of both in details?

GROUP B : Answer any three questions.

Q.6   Differentiate 8085 and 8086 Microprocessor in details?
Q.7   Write an ALP for Subtracting two numbers from registers?
Q.8   Explain interrupts structure of 8085 µp .
Q.9   Explain bus details of 8086 µp .
Q.10   Draw and explain 8255 PPI Controller?

GROUP C: All Questions are Compulsory.

Q.11 Fill in the blanks

(i)   Five bits of the flag along with 3 undefined bits is called _____.
(ii)   ALE Stands for_____.
(iii)   BIU stands for_____.
(iv)   _____ group, conditional and unconditional jumps, Call, Restart, instructions are included.
(v)   Active low means_____.

Q.12 Multiple choice question.

(i)   _____ is interface technique 8085.
        (a) Memory Mapped   (b) I/o mapped
        (c) Both   (d) None of these

(ii)   8255 is _____.
        (a) PCI   (b) DMA   (c) Both   (d) None of these
(iii)   INTR is _____ Interrupt.

(a) Hardware   (b) Software

(c) Both   (d) None of these

(iv) In 8255 pin diagram, when it goes high it resets control register and sets all port in I/P mode.

(a) $\overline{\text{Read}}$ (b) $\overline{\text{WR}}$

(c) Reset   (d) Control register

(v) All segments can start from _____.

(a) 00000H   (b) 00001H

(c) FFFFFH   (d) AAAAAH

Q.13 True or false

(i) SP is used in instruction branching.

(ii) 8086 is a 16 bit processor.

(iii) Accumulator is a fastest register.

(iv) ZERO flag is sets when the result of any operation is not equal to 00H.

(v) In 8255 µp , to specify the function of each port the control word is written into control word register.

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